

ATTACHMENT FOR SPECIFICATION AMENDMENTS

Page 4, second full paragraph:

The analog DC voltage V generated by D/A converter 150 is the input control voltage to voltage-controlled clock (VCC) 160. Based on the value of V , VCC produces an output frequency F . It should be noted that the center frequency produced by VCC 160 should have a period equal to the symbol timing period T . VCCs are well known in the art and [[is]] are not described herein. For example, the VO4OMEO1 VCC produced by Z-Communications, Inc. has a control voltage range of 0-5 Vdc (Volts DC) and has an output frequency range of 38-41 MHz (Megahertz). Edge detector [[120]] 170, then, detects the rising edge of each cycle of the output frequency F and generates a timing (interrupt) signal I which is received by distance metric computation module 120 as described above. Detecting a rising edge of a wave is known in the art and is not described herein.